1.   (Cancelled)

2.   (Cancelled)

3.   (Cancelled)

4.   (Cancelled)

5.   (Cancelled)

6.   (Cancelled)

7.   (Cancelled)

8.   (Cancelled)

9.   (Cancelled)

10.   (Cancelled)

11.   (Cancelled)

12.   (Cancelled)

13.   (Currently Amended) In an apparatus, a method of operation comprising:

a basic input/output system (BIOS) commencing a cold start reset process on re-application of AC power to the apparatus while the apparatus is in an un-powered state;

the BIOS determining as part of the cold start reset process, whether a persistent storage of the apparatus comprises a valid saved operational state of the apparatus;

the BIOS, as part of the cold start reset process, further automatically re-marking the valid saved operational state of the apparatus as invalid, if the persistent storage is determined to have a valid saved operational state of the apparatus;

the BIOS, as part of the cold start reset process, further automatically initiating a plurality of data transfer operations to transfer the saved operational state of the apparatus from the persistent storage to a memory of the apparatus to restore the saved operational state of the apparatus from the persistent storage to a memory of the apparatus;and

on completion of the data transfer operations, the BIOS setting up an immediate wake event to immediately wake the apparatus, and placing the apparatus in a suspended to memory state, resulting in the set up immediate wake event ~~to~~ immediately ~~wake~~ waking the apparatus ~~to cause~~ and causing the cold start reset process to be continued as a resume process, wherein the resume process eventually ~~leading~~ leads ~~to~~ the apparatus to start operation in an active state, ~~continuing~~ continued from the restored operational state of the apparatus.

14.   (Cancelled)

15.   (Original) The method of claim 13, wherein the method further comprises continuing with the cold start reset process, upon determining the persistent storage not comprising a valid saved operational state of the apparatus.

16.   (Cancelled)

17.   (Cancelled)

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18.   (Cancelled)

19.   (Cancelled)

20.   (Cancelled)

21.   (Cancelled)

22.   (Cancelled)

23.   (Cancelled)

24.   (Cancelled)

25.   (Cancelled)

26.   (Cancelled)

27.   (Cancelled)

28.   (Cancelled)

29.   (Cancelled)

30.   (Currently Amended) A system comprising:

a memory;

a persistent storage to store at least a saved operational state of the system; and

a basic I/O system (BIOS) equipped to be operationally coupled to the memory and the persistent storage, to perform, as part of a cold start reset process commenced

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in response to re-application of AC power to the system while the system is in an un-powered state, determining whether the persistent storage comprises a valid saved operational state of the system, marking the valid saved operational state of the system as invalid upon determining existence of the valid saved operational state of the system in the persistent storage, before or substantially concurrent with commencing restoration of the saved operational state of the system from the persistent storage to the memory, initiaing a plurality of data transfer operations to restore the saved operational state of the system from the persistent storage to the memory, and on completion of the data transfer operations, setting up a wake event to wake the system, and place the system in a suspended to memory state, resulting in the set up wake event ~~to~~ immediately ~~wake~~ waking the system ~~to cause~~ and causing the cold start reset process to continue as a resume process, wherein the resume process eventually ~~leading~~ leads ~~to~~ the system to start operation in an active state, ~~continuing~~ continued from the restored operational state of the system.

31.   (Cancelled)

32.   (Currently Amended) The system of claim 30, wherein the BIOS is further designed to continue the cold start reset process, upon determining the persistent storage does not comprise ~~of~~ a valid saved operational state of the system.

33.   (Original) The system of claim 30, wherein the system further comprises a networking interface operatively coupled to the BIOS.

34.   (Cancelled)

35.   (Cancelled)

36.   (Cancelled)

37.   (Cancelled)

38.   (Currently Amended) An article of manufacture comprising:

a storage medium;

a plurality of programming instructions stored therein, implementing a basic I/O system (BIOS) equipped to

determine as part of a cold start reset process of an apparatus initiated in response to re-application of AC to the apparatus while the apparatus is in an un-powered state, whether a persistent storage of the apparatus comprises a valid saved operational state of the apparatus,

marke the valid saved operational state of the apparatus as invalid before or substantially concurrent with commencing restoration of the saved operational state of the apparatus from the persistent storage to a memory of the apparatus,

initiate a plurality of data transfer operations to restore the saved operational state of the system from the persistent storage to the memory, and

on completion of the data transfer operations, set up a wake event to wake the system, and place the system in a suspended to memory state, resulting in the set up wake event ~~to~~ immediately ~~wake~~ waking the system ~~to cause~~and causing the cold start reset process to continue as a resume process, wherein the resume process eventually ~~leading leads~~ to the system to start operation in an active state, ~~continuing~~ continued from the restored operational state of the system.

39.   (Cancelled)

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40.   (Original) The article of claim 38, wherein the BIOS are further designed to continue and complete the cold start and reset process, after the persistent storage is determined not to comprise a saved operational state of the apparatus.